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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Parag Parikh  
Case: 2  
Serial No.: 10/770,046  
Filing Date: February 2, 2004  
Group: 2817  
Examiner: Unassigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: Jim Maurity Date: February 10, 2005

Title: Clock Generation Circuits Providing Slewing of Clock Frequency

STATUS REQUEST

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

To date, we have received no communication or official action from the Examiner in charge of the above-identified patent application. Please let us know the status of this case.

Respectfully,

Kevin M. Mason

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Date: February 10, 2005